

MALLA REDDY ENGINEERING COLLEGE (Autonomous)

Maisammaguda, Dhulapally (Post via Kompally), Secunderabad-500 100.

Department of Electronics and Communication Engineering

Minutes of Meeting M. Tech (VLSI & ES) –BOS

March 10, 2020

The Board of Studies meeting for the M. Tech (VLSI & ES) – BOS was conducted on 10th March 2020 at 10:00 A.M. in the conference hall, Malla Reddy Engineering College (Autonomous), Hyderabad.

The following BOS members have attended the meeting:

1	Dr. Madhu Babu Sikha	Assistant Professor and HoD, Dept. of ECE, MREC (A)	Chairman- BOS
2	Dr. D.Srinivasa Rao	Professor, Dept. of ECE, JNTUH CEH, Hyderabad.	University Nominee (Nominated by the Vice Chancellor, JNTUH, Hyderabad)
3	Dr.G.Mallikarjun Rao	Scientist F, RCI	Industry Expert
4	B. Devraj	Team Leader, Accenture Services Pvt.Ltd	Industry Expert
5	Mr. Vamsi Kotte	CEO, SYNTIZEN Technologies	Industry Expert
6	Dr. T. Swapna	Professor	Faculty Member
7	Dr. T.Srinivas Reddy	Associate Professor	Faculty Member
8	Dr. Subbu Lakshmi	Associate Professor	Faculty Member
9	Ms. C. Silpa	Associate Professor	Faculty Member
10	Mr J. Sunil Kumar	Assistant Professor	Faculty Member (Internal)
11	Mr. Bhalky Shiva kumar	Hardware Application Engineer, Unistring Tech Solutions	Alumni (MR16 Batch)

The following was the agenda for the meeting.

1. Discussion M. Tech. (VLSI & ES) MR20 regulations I and II year course schema and syllabus.
2. Suggestion and approval of the Professional Elective and Open Elective Courses for MR20 regulations.
3. Delegation of power to the Chairman-BOS based on recommendations of the internal committee, for the inclusion and exclusion of any item as per requirements.

At the outset, Dr. S. Madhu Babu, Assoc. Professor, ECE Department and Chairman of the M. Tech (VLSI & ES) – Board of Studies, welcomed the members to the Pre-Board of Studies meeting and appraised the purpose of the meeting. The Board of studies members have in detail discussed reviewed each item of the proposed agenda and finally the following items have been resolved unanimously by all the members of BOS for their implementation from the academic year 2020-21 and onwards.

The following resolutions are made after careful discussion and observations.

- 1) The BOS Members have thoroughly reviewed and discussed the Scheme of instructions, periods per week, and number of credits for all M. Tech (VLSI & ES) course as per the guide lines of the AICTE and JNTUH, Hyderabad. The Pre-BOS has collectively resolved to implement this scheme of Instructions, periods per week and number of credits for all 2 years of M. Tech (VLSI & ES) Course for the academic year 2020-21.
- 2) After formulation of scheme of Instructions, the Pre-BOS has devised the syllabus for M. Tech (VLSI & ES) Course as per the guide lines of the AICTE and JNTUH, Hyderabad.
- 3) The Pre-BOS has also resolved for delegation of power to the Chairman of BOS for inclusion / exclusion / modification of any of the above item as per the requirements.
- 4) After formulation of scheme of Instructions, the BOS members has reviewed and approved New courses , the following subjects are introduced for the academic year 2020-21 (MR 20).

Semiconductor Memory Design and Testing
Graphical Programming for Real-Time Applications
MEMS AND NEMS
Graph Theory and Applications
VLSI for Biomedical Systems
Automotive Embedded Systems
Wireless Multimedia Communication
High Speed Digital Design
Electronic packaging Technologies
Nano Scale Devices
Image & Video Processing
Sensors and Actuators
Wireless Sensor Networks

Finally, Dr. Madhu Babu Sikha, Assoc. Professor, ECE Department and Chairman of the M. Tech (VLSI & ES) - Board of Studies, expressed his sincere thanks to all the members of the Board of Studies for their active participation and contribution. The meeting concluded at 1:30 p.m.


Dr S Madhu Babu
Chairman, Dept. of ECE
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